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SPC-F005.DWG

REVISIONS

DOC. NO. SPC-F005 * Effective: 7/8/02 * DCP No: 1398

DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
1453	A	RELEASED	JWM	8/6/03	HO	8/6/03	DJC	8/6/03



Specifications

Electrical

- Max. Input Signal Frequency: 20MHz
- Input Impedance: 1Megohm
- Operating Supply Range: 4VDC Min., 18VDC Max.
- TTL: Logic "1" (HI LED): $>2.3 \pm 0.2VDC$
Logic "0" (LO LED): $<0.8 \pm 0.2VDC$
- CMOS: Logic "1" (HI LED): $>70\% V_{cc} \pm 10\%$
Logic "0" (LO LED): $<30\% V_{cc} \pm 10\%$
- Min. Detectable Pulse Width: 30 Nanoseconds
- Max. Signal Input Protection: $\pm 220V$ AC/DC (15 sec.)
- Power Supply Protection: $\pm 20VDC$
- Pules Indicator Flash Time: 500ms

General

- Operating Temp.: $0^{\circ}C$ to $50^{\circ}C$, 80% Relative Humidity
- Stroage temp.: $-20^{\circ}C$ to $65^{\circ}C$, 75% Relative Humidity
- Weight: 1.6 oz.
- Dimensions: 8.2 (L) x 0.7(W) x 0.7(D)

DISCLAIMER:
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TOLERANCES:
UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.

DRAWN BY:	DATE:
Jeff McVicker	8/6/03
CHECKED BY:	DATE:
Hisham Odish	8/6/03
APPROVED BY:	DATE:
Daniel Carey	8/6/03

DRAWING TITLE: Logic Probe (20MHz Frequency Displayable)			
SIZE	DWG. NO.	ELECTRONIC FILE	REV
A	72-190	52F7940.dwg	A
SCALE: NTS	U.O.M.: INCHES [mm]	SHEET: 1 OF 1	